



EV077327327

#20/Response
C. J. J. J.
4/19/02

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/420,835
Filing Date October 21, 1999
Inventor Werner Juengling
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner H. Tsai
Attorney's Docket No. MI22-1243
Title: Semiconductor Processing Methods of Forming Devices on a Substrate,
Forming Device Arrays on a Substrate, Forming Conductive Lines on a
Substrate, and Forming Capacitor Arrays on a Substrate, and
Integrated Circuitry

RESPONSE TO DECEMBER 12, 2001 OFFICE ACTION

To: Box Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

From: Frederick M. Fliegel, Ph.D.
(Tel. 509-624-4276; Fax 509-838-3424)
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601 West First Avenue, Suite 1300
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Responsive to the Office Action dated December 12, 2001, Applicant
remarks as follows:

AMENDMENTS

There are no amendments to the specification or claims.